Quiz 4  
CSE 30  
Spring 2002

#1.
The _________________ SPARC instruction causes the current register set window to slide 16 registers within the Register Stack. When this instruction completes execution, the old register set registers %____ — %_____ are now mapped to the new register set registers %_____ — %_____.

%fp of the old register set becomes %_______ of the new register set. All of this happens at the end of a closed subroutine.

The _________________ SPARC instruction saves the current value of %pc into %o7.

To access any stored parameters on the Runtime Stack in the SPARC architecture, you use a ____________ offset relative to %_____.

The _________________ SPARC instruction adds ____to the value in %o7 and sets %pc with the result.

#2.
a) Convert -69.75₁₀ to binary fixed-point and single precision IEEE floating-point representation (expressed in hexadecimal).

binary fixed-point ________________ x 2⁰

IEEE floating-point ____________________________________________ (hexadecimal)

b) Convert 0x429AC000 (single precision IEEE floating-point representation) to fixed-point decimal.

fixed-point decimal ________________________________________ (decimal / no exponential notation)